No.: 12732-008001 / US457

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Shunpei YAMAZAKI et al.

Art Unit : 2812

Serial No.: 09/769,765

Examiner: Unknown

: January 26, 2001

∄itle

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Supplemental to an information disclosure statement filed on June 12, 2002, Applicants submit the English translations for the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 11, 2002

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